



Attorney's Docket No.: 10559-515001 / P12419

#2183/EPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eidson et al.

Serial No. : 09/994,483

Filed : November 26, 2001

Title : SYSTEM-ON-CHIP BREAKPOINT SYNCHRONIZATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Art Unit : 2183

Examiner : Kevin P. Rizzuto

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	42	-	26	=	16	\$800
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Independent	8	-	8	=	0	\$0
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First Presentation of Multiple Dependent Claims						\$0
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Applicant hereby petitions under 37 C.F.R. §1.136 for a 0 month extension of time.

TOTAL FEE DUE	\$800
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A check for \$800 is attached.

Respectfully submitted,

Date: 5/23/05


Scott C. Harris
Reg. No. 32,030

BY
JOHN F. CONROY
REG. NO. 45,485

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 23, 2005

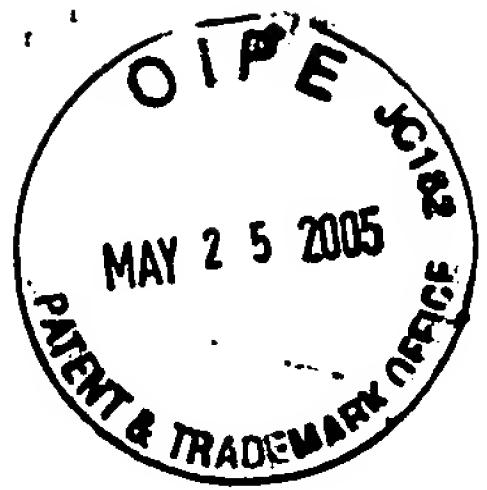
Date of Deposit



Signature

Teri Barnett

Typed or Printed Name of Person Signing Certificate



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AMENDMENT

In response to the Office action mailed February 22, 2005,
please reconsider the application in light of the following:

Amendments to the Specification beginning on page 2;

Amendments to the Claims reflected in the **Listing of Claims**
beginning on page 3; and

Remarks beginning on page 14.

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